This document is prepared from the following University Notifications

- https://www.du.ac.in/uploads/new-web/15092023_Indis_sem1.pdf
- https://www.du.ac.in/uploads/new-web/notifications-2021/28032023_nep-Faculty%20of%20Interdisciplinary%20&%20Applied%20Sciences.pdf
- https://www.du.ac.in/uploads/new-web/15092023_Indis_sem3.pdf
- https://www.du.ac.in/uploads/new-web/18092023_Inter_4.pdf

SEMESTER-V

UNIVERSITY OF DELHI

CNC-II/093/1(26)/2023-24/179
Dated: 13.09.2023

NOTIFICATION

Sub: Amendment to Ordinance V

[E.C Resolution No. 14/ (14-1-4) dated 09.06.2023]

Following addition be made to Appendix-II-A to the Ordinance V (2-A) of the Ordinances of the University;

Add the following:

Syllabi of Semester-IV, V and VI of the following departments under Faculty of Interdisciplinary and Applied Sciences based on Under Graduate Curriculum Framework -2022 implemented from the Academic Year 2022-23.

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- https://www.du.ac.in/uploads/new-web/15092023_Indis_sem1.pdf
- https://www.du.ac.in/uploads/new-web/notifications-2021/28032023_nep-Faculty%20of%20Interdisciplinary%20&%20Applied%20Sciences.pdf
- https://www.du.ac.in/uploads/new-web/15092023_Indis_sem3.pdf
- https://www.du.ac.in/uploads/new-web/18092023_Inter_4.pdf

SEMESTER-V
DEPARTMENT OF ELECTRONIC SCIENCE
Category I

(B.Sc. Honours in Electronics)

DISCIPLINE SPECIFIC CORE COURSE – 13: Embedded System

CREDIT DISTRIBUTION, ELIGIBILITY AND PRE-REQUISITES OF THE COURSE

Course title & Code	Credits	Credit distribution of the course			Eligibility criteria	Pre-requisite of the course (if any)
		Lecture	Tutorial	Practical/ Practice		
Embedded System	4	3	-	1	Class XII passed with Physics + Mathematics/Applied Mathematics + Chemistry OR Physics + Mathematics/Applied Mathematics + Computer Science/Informatics Practices	Microprocessor (DSC 11, Sem IV)

Learning Objectives

The Learning Objectives of this course are as follows:
This course introduces the student to the fundamental understanding of an embedded system. It is designed to make student familiar with the features, architectures and design issues involved in embedded system. The course focuses both on hardware and software components. Important serial communication protocols are also included. Syllabus covers microcontroller programming in C, which is platform independent.

Learning outcomes

The Learning Outcomes of this course are as follows:

- Describe the fundamental concepts and features related to embedded systems .
- Understand the AVR RISC architecture and Instruction set.
- Interface I/O devices with microcontroller using parallel ports, serial ports, ADC etc.
- Learn the concepts of hardware & software interrupts and Timer

- Design simple embedded systems including their hardware as well as software.

SYLLABUS OF ELDSC-13 **Total Hours- Theory: 45 Hours, Practicals: 30 Hours**

UNIT – I (11 Hours)

Introduction: Overview of Embedded Systems, Requirements and Applications, Introduction to microcontrollers, Harvard architecture and Von Neumann architecture, RISC and CISC microcontrollers.

AVR Microcontroller: ATMega32 AVR RISC microcontroller architecture, Status Register, General Purpose Register file, Program memory and data memory organisation, Reset sources (Power-on, Brownout & Watchdog Timer).

UNIT – II (11 Hours)

Instruction Set: Addressing Modes, Data Transfer Instructions, Arithmetic and Logic Instructions, Branch Instructions, Bit and Bit-test Instructions, MCU Control Instructions., Introduction to AVR Programming in C, C datatypes, operators for AVR, simple programs for control, loop, arithmetic & logical operations and bit manipulation.

UNIT – III (12 Hours)

Peripheral I: Configuring I/O ports, Pull-up resistors, reading and writing data to I/O ports. Introduction to Interrupts, interrupt vector address and priority, ISR, External Interrupts. Introduction to Timers, Timers as delay generators and event counters, Timer0 modes of operation.

UNIT – IV (11 Hours)

Peripheral II: Analog-to-Digital Converter (ADC), Basics of Serial Communication, Universal Synchronous and Asynchronous serial Receiver and Transmitter (USART), Serial Peripheral Interface (SPI), Two Wire Interface (TWI) / I2C bus.

Practical component (if any) – Embedded System

(Hardware and AVR studio or similar IDE Software)

(Students are required to perform listed experiments and make a Mini Project)

Learning outcomes

The Learning Outcomes of this course are as follows:

- Student will be able to program AVR microcontrollers using AVR studio/similar IDE.
- Learn different interfacing techniques and standards to control various input output devices with the microcontroller.
- Student will be equipped with sufficient knowledge to implement mini projects.

LIST OF PRACTICALS (Total Practical Hours- 30 Hours)

1. (i) Blink LED at a constant rate.
   (ii) Blink LED at linearly increasing rate until the LED appears always on.
2. Use LFSR (linear feedback shift register) based random number generator to generate a random number and display it.
3. To interface 4 Keys with Port A and Port B each. Write a program to read the data from Port A and Port B and display its sum (and other arithmetic & logical operations) on output device.
4. To interface a LED/Buzzer with an o/p pin of AVR microcontroller. Write a program to blink the LED / Beep the Buzzer at (i) a constant rate (ii) linearly increasing rate using Timer.
5. To interface a 4x4 Keypad/push button keys with I/O pins of AVR microcontroller. Write a program to display the number of the key pressed in Binary number format on LED array or decimal number format on 7-segment LED or text display on an LCD or Serial Monitor.
6. To interface a potentiometer with ADC of AVR microcontroller. Write a program to display the dc input voltage on an output device (LED array / 7-segment LED / LCD / Serial Monitor).
7. To control the intensity of an LED/pitch of buzzer using PWM mode of Timer 0.
8. To interface a DC motor or Stepper motor and to write a program to control its speed.

Mini Project

(Any one of the following mini project or on similar concepts incorporating data acquisition from sensors/ input device, data analysis & control and display of result on any output device) (individual project only)

Project Idea 1: Weather Monitoring System -
Input - Temperature, humidity, wind speed etc.
Output - Display instantaneous values, average value, MAX / MIN value and predicted value for the next hour

Project Idea 2: Electronic Voting Machine -
Input - 8 Voting keys, Control Keys (Master Clear, Display Result, etc)
Output - Display device showing instructions, messages and results in accordance to the key pressed

Project Idea 3: Health Monitoring System -
Input – Pulse rate, Blood Pressure, SpO2, etc.
Output - Display device showing results

Note: Students shall sincerely work towards completing all the above listed practicals for this course. In any circumstance, the completed number of practicals shall not be less than seven and make a Mini Project.

Essential/recommended readings

1. "AVR Microcontroller and Embedded Systems: Using Assembly and C", Muhammad Ali Mazidi, Sarmad Naimi, Sepehr Naimi, PHI,2013
2. "Programming and Customizing the AVR Microcontroller", D V Gadre, McGraw- Hill,2000
3. "Atmel AVR Microcontroller Primer: Programming and Interfacing", Steven F. Barrett, Daniel J. Pack, Morgan & Claypool Publishers,2012
4. "Embedded system Design", Frank Vahid and Tony Givargis, John Wiley, 2002

Suggestive readings

1. "An Embedded Software Primer", David E Simon, Addison Wesley,1999
2. AVR Microcontroller Datasheet, Atmel Corporation, www.atmel.com

Note: Examination scheme and mode shall be as prescribed by the Examination Branch, University of Delhi, from time to time.

DISCIPLINE SPECIFIC CORE COURSE – 14: Electromagnetics

CREDIT DISTRIBUTION, ELIGIBILITY AND PRE-REQUISITES OF THE COURSE

Course title & Code	Credits	Credit distribution of the course			Eligibility criteria	Pre-requisite of the course (if any)
		Lecture	Tutorial	Practical/ Practice		
Electromagnetics	4	3	-	1	Class XII passed with Physics + Mathematics/ Applied Mathematics + Chemistry OR Physics + Mathematics/ Applied Mathematics + Computer Science/ Informatics Practices	Engineering Mathematics (DSC 7, Sem III)

Learning Objectives

The Learning Objectives of this course are as follows:
The syllabus of the paper is very carefully framed with the objective to well verse the students of the programme about

- Ability to apply knowledge of mathematics in solving electromagnetic problems.
- To understand the concept of electromagnetic waves in low frequency and high frequency applications.
- This paper is the backbone in the development of new integrated devices and applications of electromagnetic principles in various allied disciplines such as communications, microwaves, radar, electromagnetic interference & electromagnetic compatibility, remote sensing and fibre optics.
- Basic laws of electromagnetics required for any student who wants to pursue his career in research

Learning outcomes

The Learning Outcomes of this course are as follows:

- Getting familiar with vector algebra, coordinate system and coordinate conversion
- Understanding electrostatic fields and magnetostatic fields.
- A balanced presentation of static and time-varying fields.

- Physical interpretation of Maxwell's equation and problem solving in different media
- Understanding of propagation of an electromagnetic wave.

SYLLABUS OF ELDSC-14 **Total Hours- Theory: 45 Hours, Practicals: 30 Hours**

UNIT – I (14 Hours)

Vector Analysis: Scalars and Vectors, Vector Algebra, Rectangular (Cartesian) Coordinate System, Vector Components and Unit Vector, Vector Field, Products, Cylindrical Coordinates, Spherical Coordinates, Differential Length, Area and Volume, Line Surface and Volume integrals, Del Operator, Gradient of a Scalar, Divergence and Curl of a Vector ,Divergence and Stokes Theorem, the Laplacian.

Electrostatic Fields: Coulomb's Law and Electric Field, Electric Potential, Electric Flux Density, Gauss's Law and Applications, Divergence Theorem and Maxwell's First Equation, Electric dipole. Electric Fields in Conductors, Current and Current Density, Continuity of Current, Metallic Conductor. Dielectric materials, Polarization in Dielectrics, Dielectric Constant, Isotropic and Anisotropic dielectrics. Electrostatic Energy, Boundary Condition, Poisson equation and Laplace equation, Uniqueness Theorem.

UNIT – II (10 Hours)

Magnetostatics: Biot Savert's law, Magnetic dipole, Ampere's Circuital Law, Maxwell's Equation, Magnetic Flux and Magnetic Flux Density, Scalar and Vector Magnetic Potentials. Magnetization in Materials and Permeability, Anisotropic materials. Magnetic Energy, Boundary Conditions

UNIT – III (10 Hours)

Time-Varying Fields and Maxwell's Equations: Faraday's Law of Electromagnetic Induction, stationary and moving loop in time varying magnetic field, Displacement Current, Maxwell's Equations in differential and integral form and Constitutive Relations. Time varying potential, Lorentz condition for potential. Wave Equation for Potentials. Time Harmonic Electromagnetic Fields and use of Phasors

UNIT – IV (11 Hours)

Electromagnetic Wave Propagation: The Electromagnetic Spectrum, Wave Equation in a source free isotropic homogeneous media, Uniform Plane Waves propagation in Lossless and Lossy unbounded homogeneous media, Plane Wave Propagation in Good conductor, wave Impedence, Skin Depth and skin effect, Wave Polarization: Linear, elliptical and Circular. Flow of Electromagnetic Power and Poynting Vector.

Practical component (if any) – Electromagnetics
(using Scilab/MATLAB/ any other similar freeware)

Learning outcomes

The Learning Outcomes of this course are as follows:

- Understand the plotting of vectors, and transformation among various coordinate systems in 2D and 3D.
- Understand the graphical representation of scalar and vector fields including gradient, divergence and curl.
- Understand the graphical representation of electric and magnetic fields for various types of charge and current distributions respectively.
- Understand the flow of energy and power associated with electromagnetic waves.

LIST OF PRACTICALS (Total Practical Hours- 30 Hours)

1. Understanding and Plotting Vectors.
2. Point to point and Vector Transformation from Cartesian to cylindrical co-ordinate system and vice versa.
3. Point to point and Vector Transformation from Cartesian to Spherical co-ordinate system and vice versa.
4. Point to point and Vector Transformation from Cylindrical to Spherical co-ordinate system and vice versa.
5. Representation of the Gradient of a scalar field, Divergence and Curl of Vector Fields.
6. Plots of Electric field due to charge distributions.
7. Find the Magnetic field from a given Electric field for a Uniform plane wave.
8. Find a Poynting Vector for a given electromagnetic field at a given point.

Note: Students shall sincerely work towards completing all the above listed practicals for this course. In any circumstance, the completed number of practicals shall not be less than seven.

Essential/recommended readings

1. Murray. R. Spiegel, Vector Analysis, Schaum series, Tata McGraw Hill (2006)
2. M. N. O. Sadiku, Elements of Electromagnetics, Oxford University Press (2001)
3. D. C. Cheng, Field and Wave Electromagnetics, Pearson Education (2001)
4. J. A. Edminster, Electromagnetics, Schaum Series, Tata McGraw Hill (2006)
5. Introduction to Electrodynamics, D.J. Griffiths, Pearson Education (2012)
6. Electromagnetic Wave and Radiating System, Jordan and Balmain, Prentice Hall (1979)

Suggestive readings

1. N. Narayan Rao, Elements of Engineering Electromagnetics, Pearson Education (2006)
2. W. H. Hayt and J. A. Buck, Engineering Electromagnetics, Tata McGraw Hill (2006)

Note: Examination scheme and mode shall be as prescribed by the Examination Branch, University of Delhi, from time to time.

DISCIPLINE SPECIFIC CORE COURSE – 15: Basic VLSI Design

CREDIT DISTRIBUTION, ELIGIBILITY AND PRE-REQUISITES OF THE COURSE

Course title & Code	Credits	Credit distribution of the course			Eligibility criteria	Pre-requisite of the course (if any)
		Lecture	Tutorial	Practical/ Practice		
Basic VLSI Design	4	3	-	1	Class XII passed with Physics + Mathematics/Applied Mathematics + Chemistry OR Physics + Mathematics/Applied Mathematics + Computer Science/Informatics Practices	Semiconductor Devices(DSC 3, Sem I), Digital Electronics(DSC 5, Sem II)

Learning Objectives

The Learning Objectives of this course are as follows:
This course introduces the student to basic principle of MOS Transistor operation, SPICE model, MOS transistor and Inverter layout, CMOS layout, Inverter design, CMOS inverter, inverter characteristics and specifications. Static and Sequential MOS Logic design, pass transistor logic, static & dynamic latches, flip flops, static & dynamic registers, Monostable sequential circuits. MOS memory design, RAM & ROM cells, Logic families performance.

Learning outcomes

The Learning Outcomes of this course are as follows:

- Understand the concept of models of MOS devices and their implementation in designing of CMOS inverter
- Measure the performance parameters like threshold voltage, noise margins, time delays etc.
- Familiarize with the techniques and components involved in combinational MOS circuit designs.
- Describe the various types of semiconductor memories and issues involved in them

SYLLABUS OF ELDSC-15 Total Hours- Theory: 45 Hours, Practicals: 30 Hours

UNIT – I (12 Hours)

Metal Oxide Semiconductor (MOS): Introduction to basic principle of MOS transistor, large signal MOS models (long channel) for digital design. MOS SPICE model, MOS Transistor layout(PMOS and NMOS)

UNIT – II (12 Hours)

MOS Inverter: Inverter principle, Depletion and enhancement load inverters, the basic CMOS inverter, transfer characteristics, logic threshold, Noise margins, Dynamic behaviour, Propagation Delay and Power Consumption.

UNIT – III (11 Hours)

Combinational MOS Logic Design: Static MOS design, Pass Transistor logic, complex logic circuits.

Sequential MOS Logic Design - Static latches, Flip flops & Registers, Dynamic Latches & Registers, Monostable sequential circuits.

UNIT – IV (10 Hours)

Memory Design: ROM & RAM cells design. Dynamic MOS design- Dynamic logic families and performances.

Design for testability: Introduction, Fault types and models, Controllability and observability, AdHoc Testable design techniques, Scan –based techniques.

Practical component (if any) – Basic VLSI Design
(PSpice/other Simulation Software)

Learning outcomes

The Learning Outcomes of this course are as follows:

- Reproduce the characteristics of digital circuits like inverter and other logic gates based on CMOS technology.
- Design the digital circuit components like latches, multiplexers etc.
- Perform experiments and the circuit design and collect and analyse the data
- Prepare the technical report on the experiments carried

LIST OF PRACTICALS (Total Practical Hours- 30 Hours)

1. To plot the (i) output characteristics & (ii) transfer characteristics of an n-channel and p-channel MOSFET.
2. To design and plot the static and dynamic characteristics of a digital CMOS inverter.
3. To design and plot the output characteristics of a 3-inverter ring oscillator.
4. To design and plot the dynamic characteristics of 2-input NAND, NOR, XOR and XNOR logic gates using CMOS technology.
5. To design and plot the characteristics of a 4x1 digital multiplexer using pass-transistor logic.
6. To design and plot the characteristics of a positive and negative latch/master-slave edge triggered registers based on multiplexers.

7. To prepare layout for given logic function and verify it with simulations.
   To measure propagation delay of a given CMOS Inverter circuit.

Note: Students shall sincerely work towards completing all the above listed practicals for this course. In any circumstance, the completed number of practicals shall not be less than six.

Essential/recommended readings

1. Weste and Eshraghian, —Principles of CMOS VLSI design, Addison-Wesley, 2002.
2. Basic VLSI design: Douglas A Pucknell, Kamran Eshraghian, PHI, 3rd edition

Suggestive readings

1. Kang & Leblebigi —CMOS Digital IC Circuit Analysis & Design- McGraw Hill, 2003.
2. Rabey, —Digital Integrated Circuits Design, Pearson Education, Second Edition, 2003.

Note: Examination scheme and mode shall be as prescribed by the Examination Branch, University of Delhi, from time to time.